



Docket No.: 0941-1717PUS1  
(PATENT)

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Patent Application of:  
Masakatsu YAMASHITA

Application No.: 10/526,420

Confirmation No.: 3082

Filed: March 1, 2005

Art Unit: 2629

For: IMAGE DISPLAY DEVICE WITH CIRCUITS  
TO COMPENSATE VOLTAGE DROP IN THE  
COMMON ELECTRODE FOR ACTIVE  
MATRIX LIQUID CRYSTAL DISPLAYS

Examiner: B. Q. T. Ho

PLEASE ENTER

**AMENDMENT AFTER FINAL OFFICE ACTION**

/BTH/

MS AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**INTRODUCTORY COMMENTS**

In response to the Examiner's Office Action dated June 24, 2008, Applicant respectfully submits the following amendments and remarks in connection with the above-identified application.

**This reply includes**

**Amendments to the Claims; and**

**Remarks.**